Tele: 0532-2256762



C.M.P. Degree College, Allahabad

(A Constituent College of Allahabad University) M.G. Marg, Allahabad- 211002

Ref No. CMP/1061/2023

Date: 19.10.2023

LTE

Dear Sir/Madam

C.M.P.Degree College invites LTE from registered firms for purchase of laboratory equipments for DCA department. Quotations are invited from individual registered firms by registered post up to 2.00 P.M. on 06.11.2023 and shall be opened at 2.30 P.M. on the same day. Please quote your rates in attached bill of quantity. Interested bidders may download the tender documents from the college web-site address: www.cmpcollege.ac.in. CMP/106/ / 2023 on envelope.

CIAI I	Description of items	Quantity	Amount
SI.No	Description of items		
1	List enclosed		

- While submitting the quotation following should invariably be mentioned:
 - a) Details of specification.
 - b) Lowest rate F.O.R. destination.
 - c) Discount, if any.
 - d) GST at confessional rate as applicable to the Educational Institutions.
 - e) Period of validity.
 - f) Firm delivery time from the date of receipt of confirmed order, condition of supply and terms of payment.

- 1. Under no circumstances unsealed quotation will be entertained in the office.
- Quotations received after the due date shall not be considered.

Purchase Committee

Principal 19 10.2)

Technical specifications required for Digital Electronics laboratory in DCA

S No	Items with specifications	Required un
		04
1.	LOGIC GATES: UNIVERSAL GATES (NAND & NOR GATES).	
	5 Volts /150 m Amps D.C. Regulated Power Supply.	
	2 Logic 1 & 2 Logic '0' Inputs provided on 4 mm output sockets on the engraved front panel with 2 Red 11 D Output Indicators 4 "NAND & 4" NOR" Gates & their respective LC's placed	
	with 2 Red 11 D Chilput Indicators 4 NAND & 4 NOR Gales &	
-	internally below the panel	04
H Fe m	Half & Full Digital Adders has been designed to verify the truth table for Digital Half Adder &	
	Half & Full Digital Adders has been designed to verify the fruit table (Euro 7486 are Full Adder Fixed output DC tegulated Power supply of SV Two EX-OR Gates, IC no 7408	
	Full Adder Fixed output DC regulated Power supply of SV TWO EACH TWO AND Gates. IC no 7408 mounted made the cabinet & connections brought out on sockets. Two 'AND' Gate, IC no 7432	
	are mounted inside the cabinet & connections brought out on sockets. One 'OR' Gate, IC no 7432 are mounted inside the cabinet & connections brought out on sockets. Three Logic inputs, logic	
	are mounted inside the cabinet & connections brought out on sockets. Three Logic inputs, logic is mounted inside the cabinet & connections brought out on sockets. Three Logic inputs, logic is mounted inside the cabinet & connections brought out on sockets.	
	"0" & logic "1" selectable using SPDT switches. Two output indicators mounted on the front	
	panel to observe the output status	04
3.	4 BIT ADDER AND SUBTRACTOR. Legisle using SPDT switches, 4	
	DC Regulated Power Supply 5V/150mA, 4 Logic inputs selectable using SPDT switches, 4	
	output LFD indicators, circuit diagram printed & connections brought to	
	4 BIT MAGNITUDE COMPARATOR TRAINER.	0.3
4.		
	The state of the state of the sector of the	
	Coloured Sockets Patch Cords 2mm. The trainer is housed in ABS Plastic cabinet Instruction	
-	ENCODER & DECODER CIRCUITS.	03
5.	the state of the s	
	1 1 6 1117 suspection elect pulse 4 output indicators, circuit diagram for the 1970, 10	
	segment display printed & connections for various inputs & outputs brought out at the sockets on	
	the front page!	04
6.	MULTIPLEXER AND DEMULTIPLEXER TRAINER.	
	Technical specifications: DC Power Supply +5V,150mA,Operated on Mains power 230V, 50Hz +10%,Twenty-One	
	the first based ments to colors High / Low Lil. Icycly, cacil with a LELF to more men	
	1 11 Annual Assessment on Civien independent bullered logic level indicators for right Low	
	1 - A contain awar coboard it for Mulliplexing (4150 and (4155 is provided on	
	1	
	with Combinational Circuit, Front panel built with high class insulated Printed Circuit Board	
	sheet with printed circuits and symbols. Connections are brought out through 2mm Sockets. Patch Cords 2mm. The trainer is housed in ABS Plastic cabinet. Instruction manual	
_	RS, D, T, JK & JK MASTER SLAVE USING NAND GATE FLIP FLOP.	04
	TECHNICAL SPECIFICATIONS:	
	DC nor Supply +5V 150mA *Operated on Mains power 230V, 50Hz +10%, Four	
	independent long level inputs to select High / Low TTL levels, each with a LED to indicate high	
	/ low status and termination. Two independent buffered logic level indicators for High / Low	
	statu. A Pulser to provide the pulses manually for triggering. One 7473 IC provided on board for JK and T type Flip Flop. One 7474 IC Provided on the board for D Type Flip Flop. One RS Flip	
	Flop Circuit is provided on Board using TTL Gate. One JK Master Slave Flip Flop is provided	
	on Board using TTL Gate. One JK Flip Flop is provided on Board using TTL Gate. Front panel	
	built with high class insulated Printed Circuit Board sheet with printed circuits and symbols.	
	Connections are brought out through 2mm Coloured Sockets Patch Cords 2mm. The trainer is	
	housed in ABS Plastic cabinet/Wooden Instruction manual	
8.	4 BIT UP / DOWN COUNTER, Training Board consists of one fixed D.C. Regulated Power	04
	Supply, I.C. 74193 is placed inside and connection brought out on panel, 2 Sockets for up &	
	sown count pins. 4 Logic Output Indicators &	
0	1 HZ monoshot clock pulse switch also provided on the front panel. 4 BIT RIPPLE COUNTER.	04
9.	4 DIT RILLER COMMITTEE	

Dr. Sarita Srivastava Convener DCA Dr. S.K. Mishra
Convener, Purchase Committee

Prof. Ajay Prakash Khare Principal